IC for consumer electronics
IC for TV sets

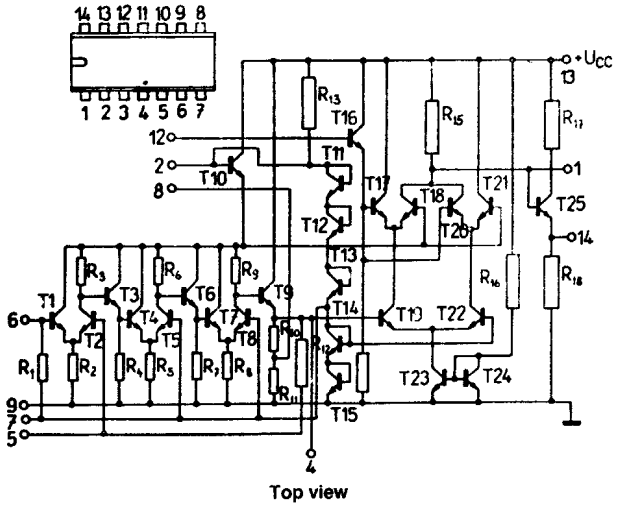
I. F.—F. M. AMPLIFIER

MAA661

Maximum ratings

U_{CC}	max.	+15	V
$U_{5/6}$	max.	±4	V
P_{tot}	max.	600	mW
ϑ_a	max.	0 ... +70	°C
ϑ_{stg}	max.	−55 ... +155	°C

The negative voltage can not be used on the circuit.



Top view

Package: G1—4D

Characteristic data

$\vartheta_a = 25$ °C, $U_{CC} = +12$ V

		nom.	min.—max.	
Static data:				
Total supply current	I_{CC}	13	8 ... 18	mA
Voltage on lead 2, input of detector	U_2	3,7		V
Voltage on lead 6, input of amplifier	U_6	1,45		V
Voltage on lead 4, output of amplifier, high level	U_4	1,5		V
Voltage on lead 8, output of amplifier, low level	U_8	0,145		V
Voltage on lead 14, A. F. output	$U_{14\,AF}$	7,00		V
Dynamic data: I. F. amplifier $f = 6{,}5$ MHz				
Voltage amplifier gain $U_I = 100$ µV	A_u	60		dB
Input voltage for amplifier limiting $\Delta f = \pm 50$ kHz, $f_{mod} = 1$ kHz	U_{om}	70	< 350	µV
Output A. F. voltage from detector $U_I = 10$ mV, $\Delta f = \pm 50$ kHz, $f_{mod} = 1$ kHz	$U_{O\,AF}$	1000	> 500	mV
A. M. rejection $U_I = 10$ mV, $\Delta f = \pm 50$kHz, $f_{mod} = 1$ kHz, modul. 30 %	AMR	50	> 40	dB
Output A. F. signal distortion $U_I = 10$ mV, $\Delta f = \pm 25$ kHz, $f_{mod} = 1$ kHz	K	1		%
Load resistance	R_L		> 2	kΩ
Amplifier input resistance	R_I	3,5		kΩ
Detector input resistance (lead 12)	$R_{I\,12}$	70		kΩ
Amplifier output resistance (lead 8)	$R_{O\,8}$	60		Ω
Detector output resistance (lead 14)	$R_{O\,14}$	100		Ω